

Claims:

1 1. A system for protecting memory, comprising:
2 memory for storing access logic and parameters; and
3 circuitry for executing the access logic in relation to
4 the parameters that grant access to memory only to resident
5 applications on the computer that are authorized to gain
6 access to the memory.

1 2. The system of claim 1 wherein port access is only
2 granted to external sources identified as known good external
3 sources within the memory access parameters.

1 3. The system of claim 1 wherein the logic creates,
2 upon execution by the processor, a plurality of filters that
3 block access to memory.

1 4. The system of claim 3 comprising a plurality of
2 caches that operate with the plurality of filters to
3 determine, on a prompt basis, whether a routine, whether
4 internal or external, is to be given access to memory.

- 1 5. A method for protecting a computer system from
- 2 attacks by hackers, comprising:
- 3 examining access logic in relation to an application
- 4 seeking access to a specified system element; and
- 5 determining whether to allow access by the application.

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